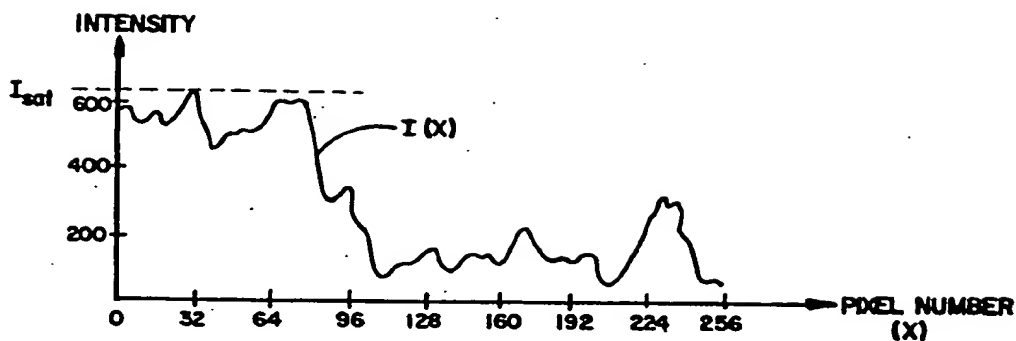
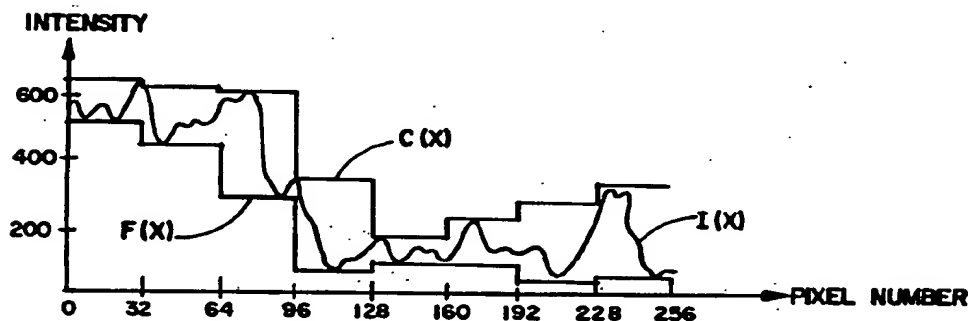
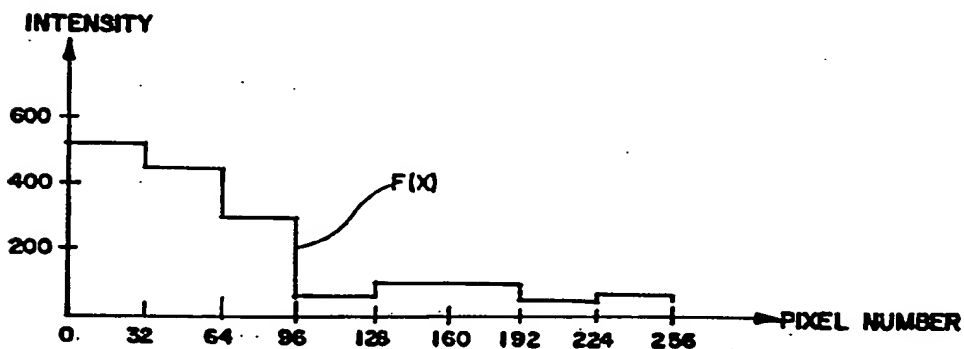
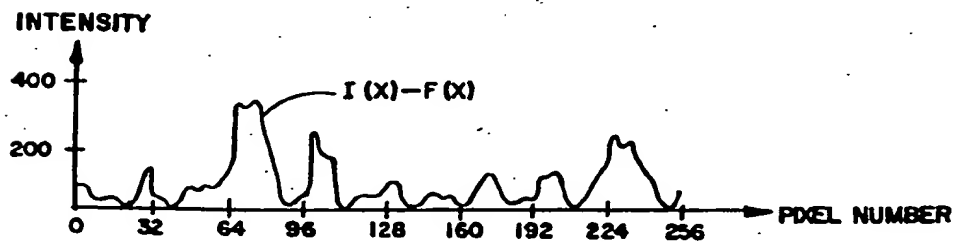
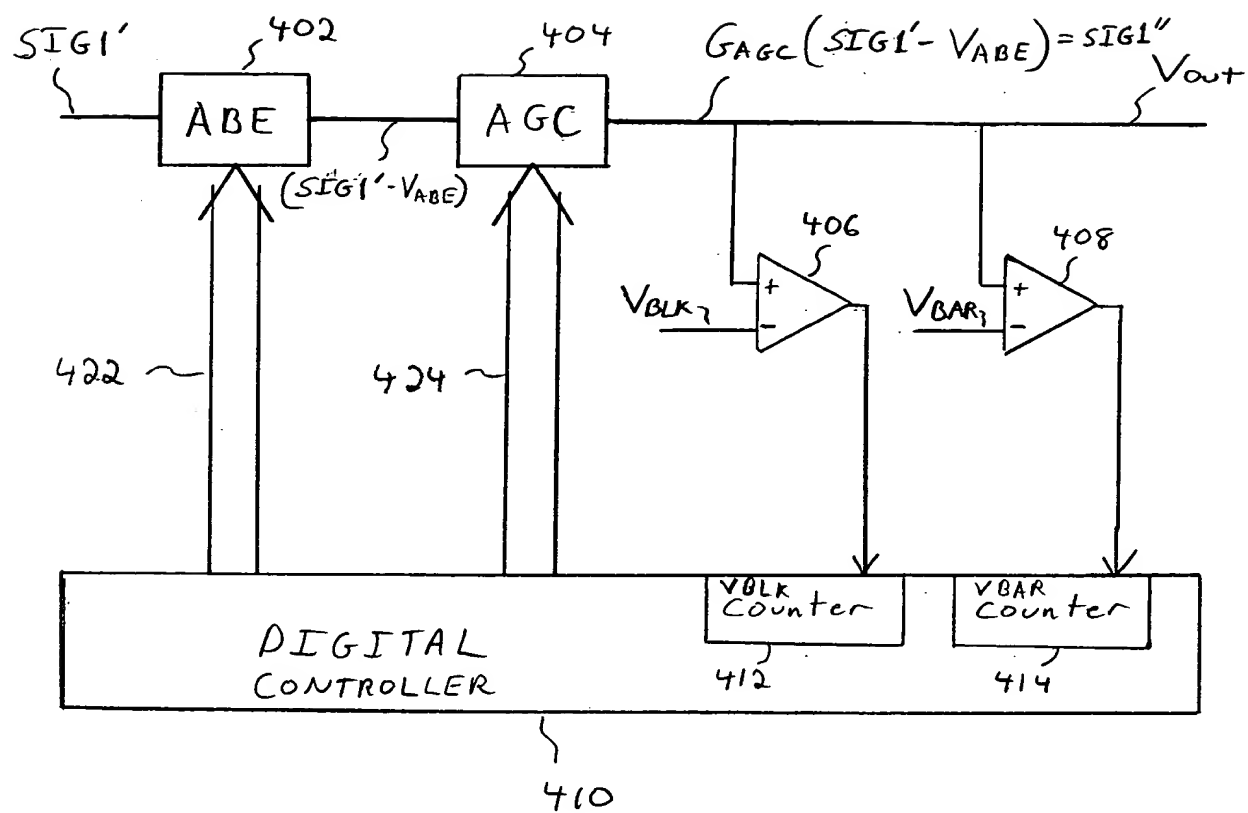


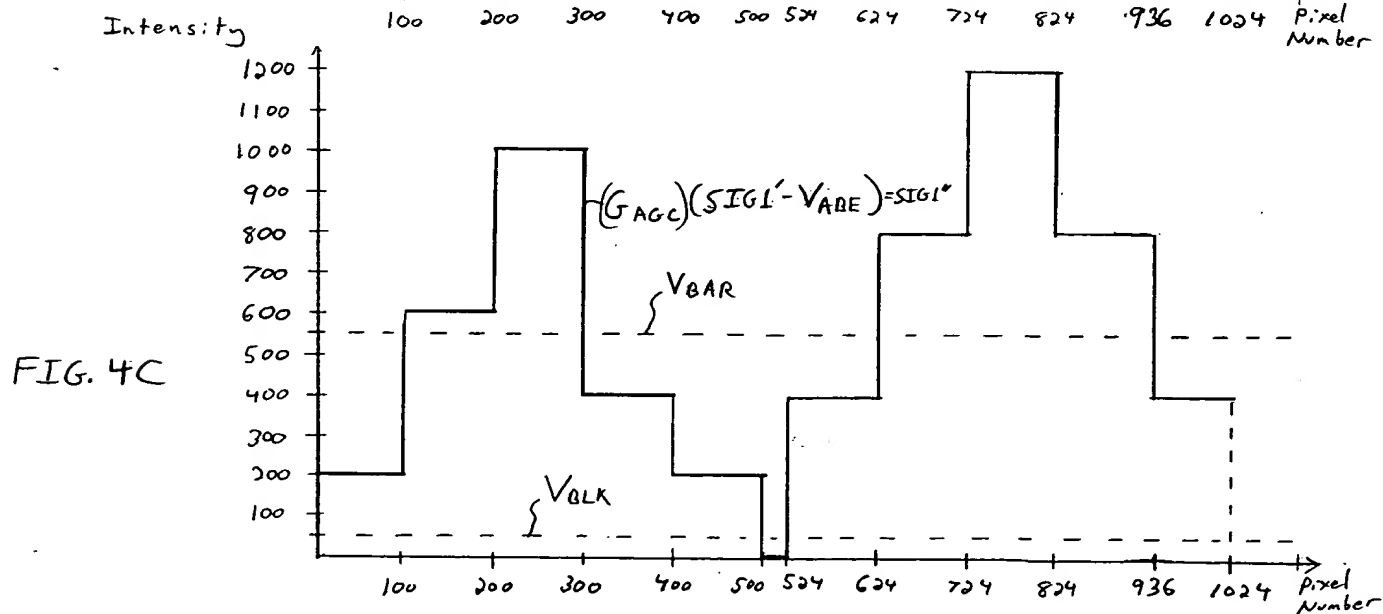
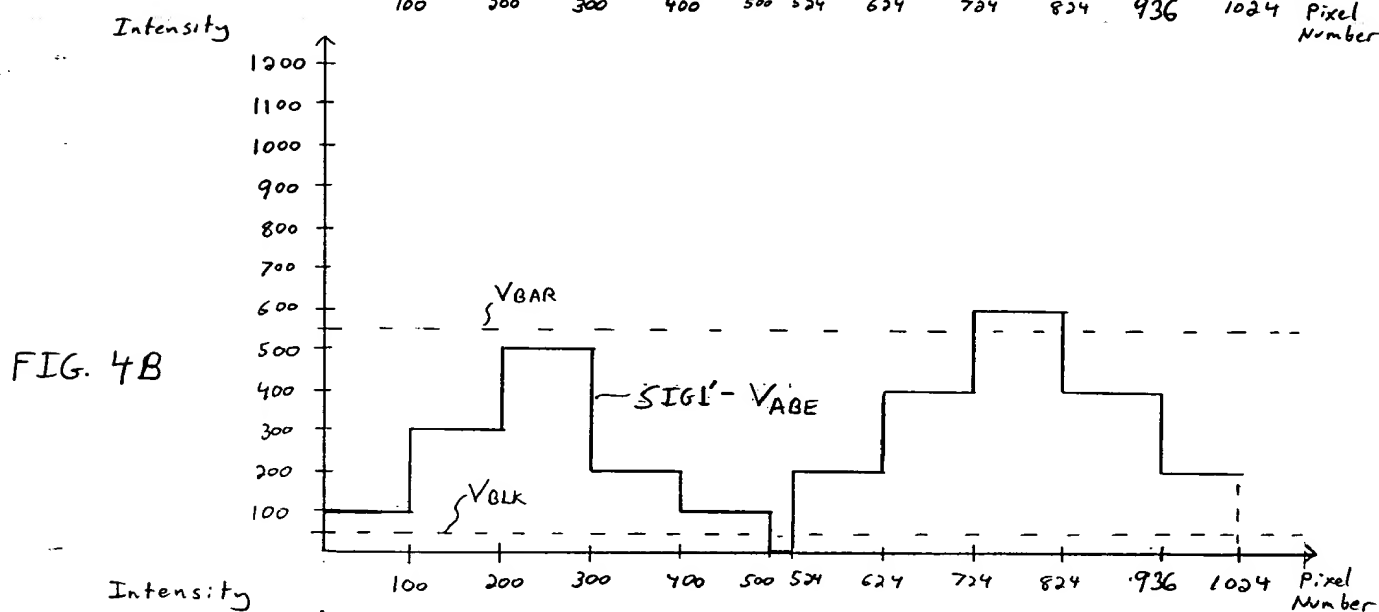
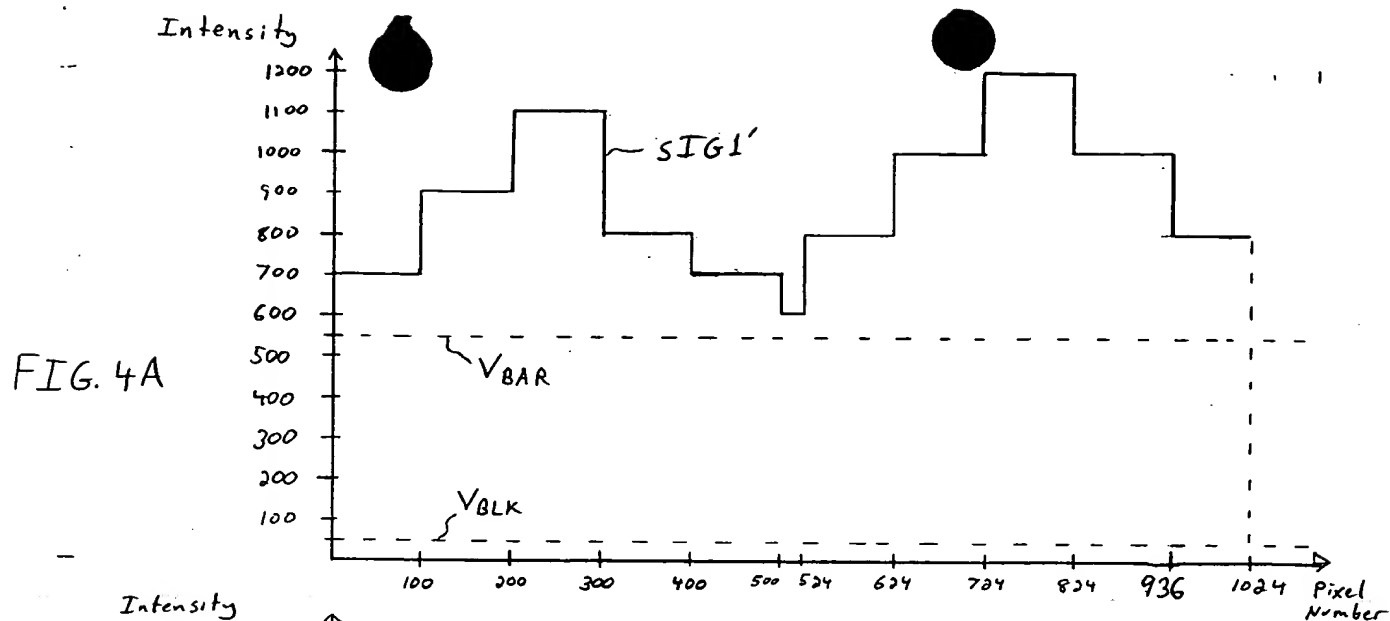
The block diagram illustrates a pixel bias correction system. A **SIGNAL SOURCE** (11) provides **NEW DATA (ND)** to a summing junction (21). The signal path includes a **DELAY** block (19) and a **GAIN FUNCTION GEN.** block (17). An **"I-GETTER" (FIG. 12)** (13) receives an **INITIALIZE COMMAND** and outputs **BIAS PARAMETERS** to a **BIAS FUNCTION GEN. (FIG. 20)** (15) and **GAIN PARAMETERS** to the **GAIN FUNCTION GEN.** (17). The **BIAS FUNCTION GEN.** (15) outputs a **PIXEL BIAS CORRECTION** signal to the summing junction (21). The summing junction (21) also receives a feedback signal from the output of the **GAIN FUNCTION GEN.** (17). The output of the summing junction (21) is then processed by a **GAIN FUNCTION GEN.** block (23) before being sent **TO DISPLAY GENERATOR**.

FIG. 1
(prior art)

[illegible][illegible][illegible][illegible]

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↓





660260 01566660

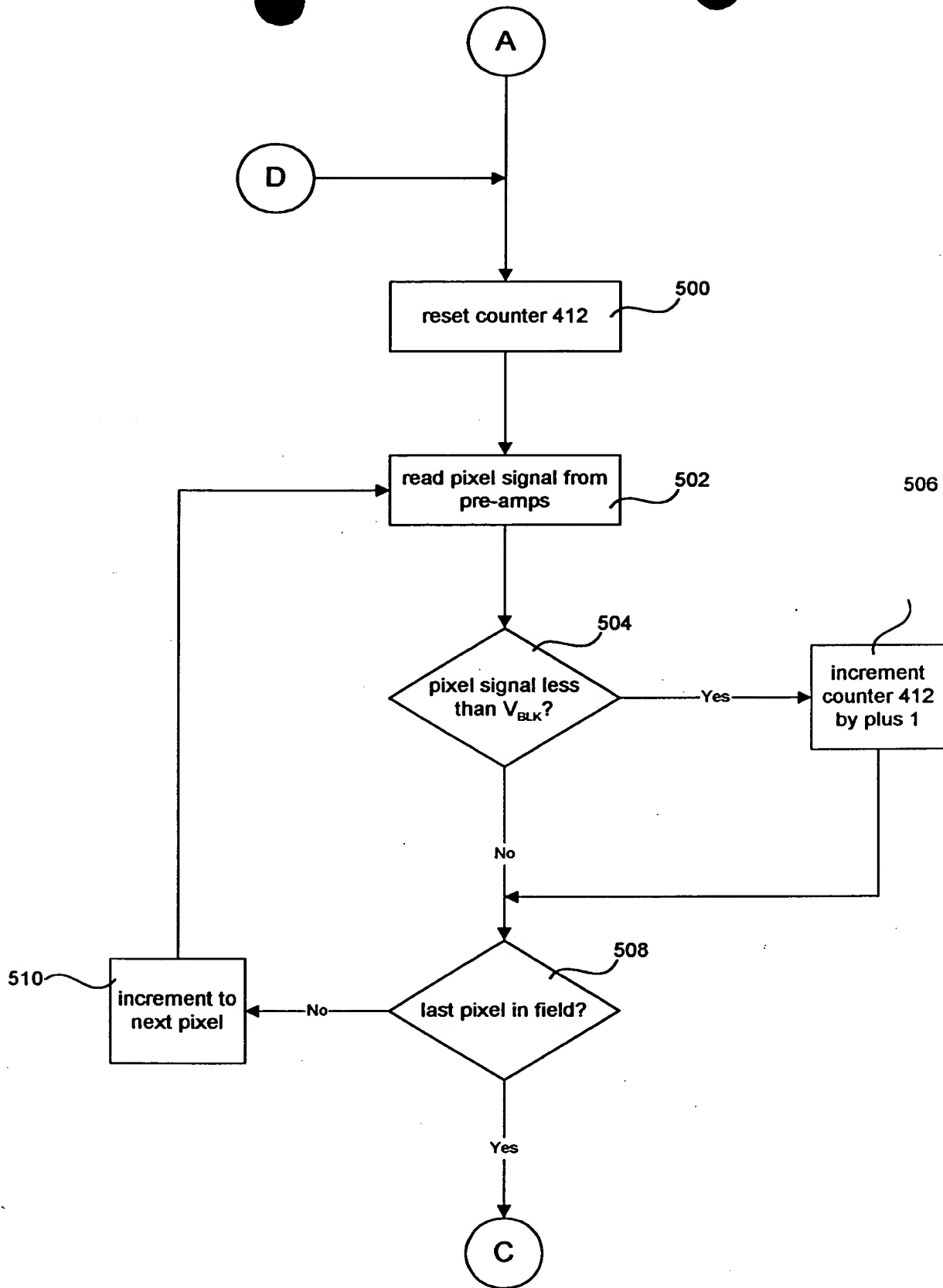


FIG. 5A

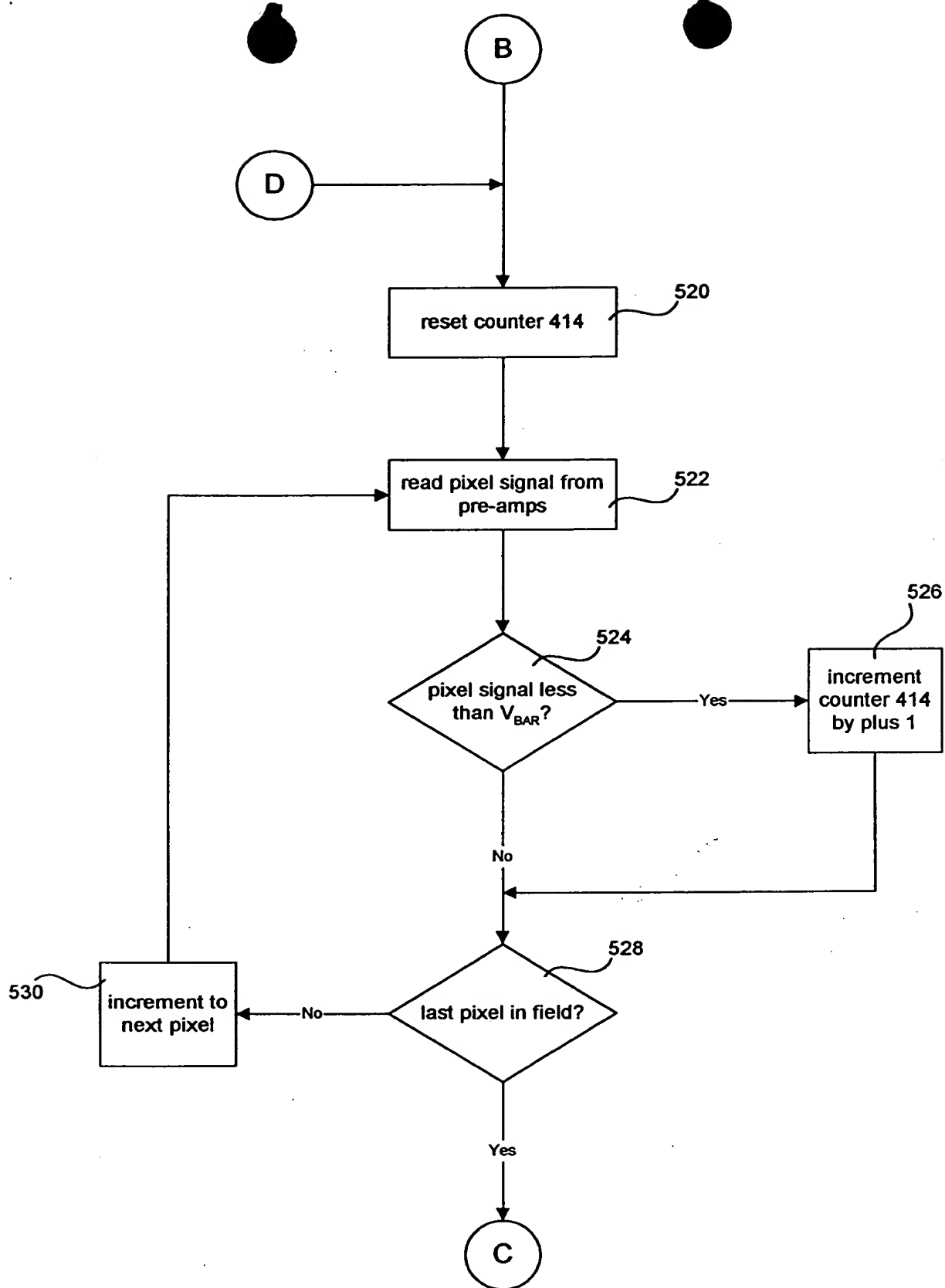


FIG. 5B

6502250" 015606E60

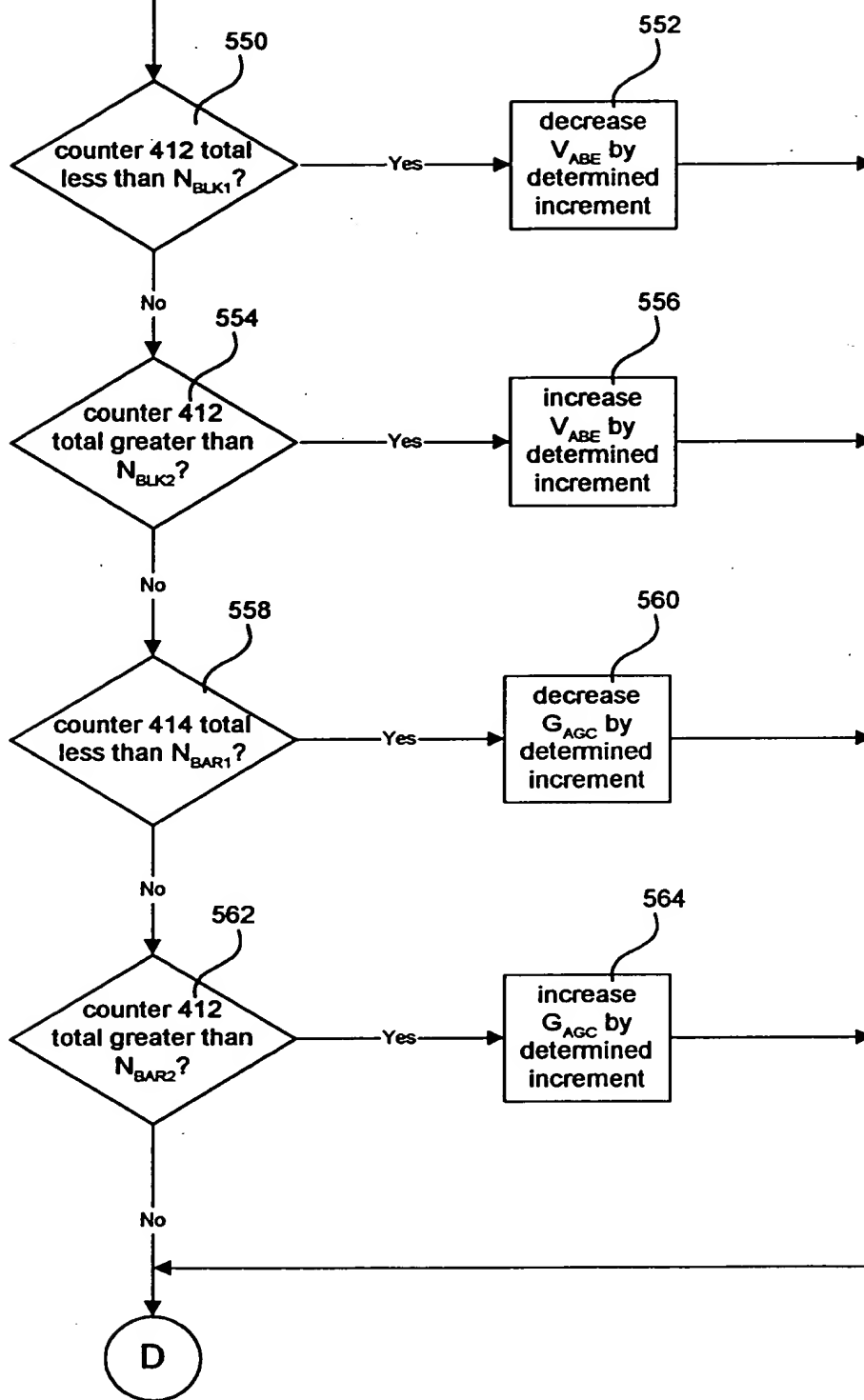


FIG. 5C